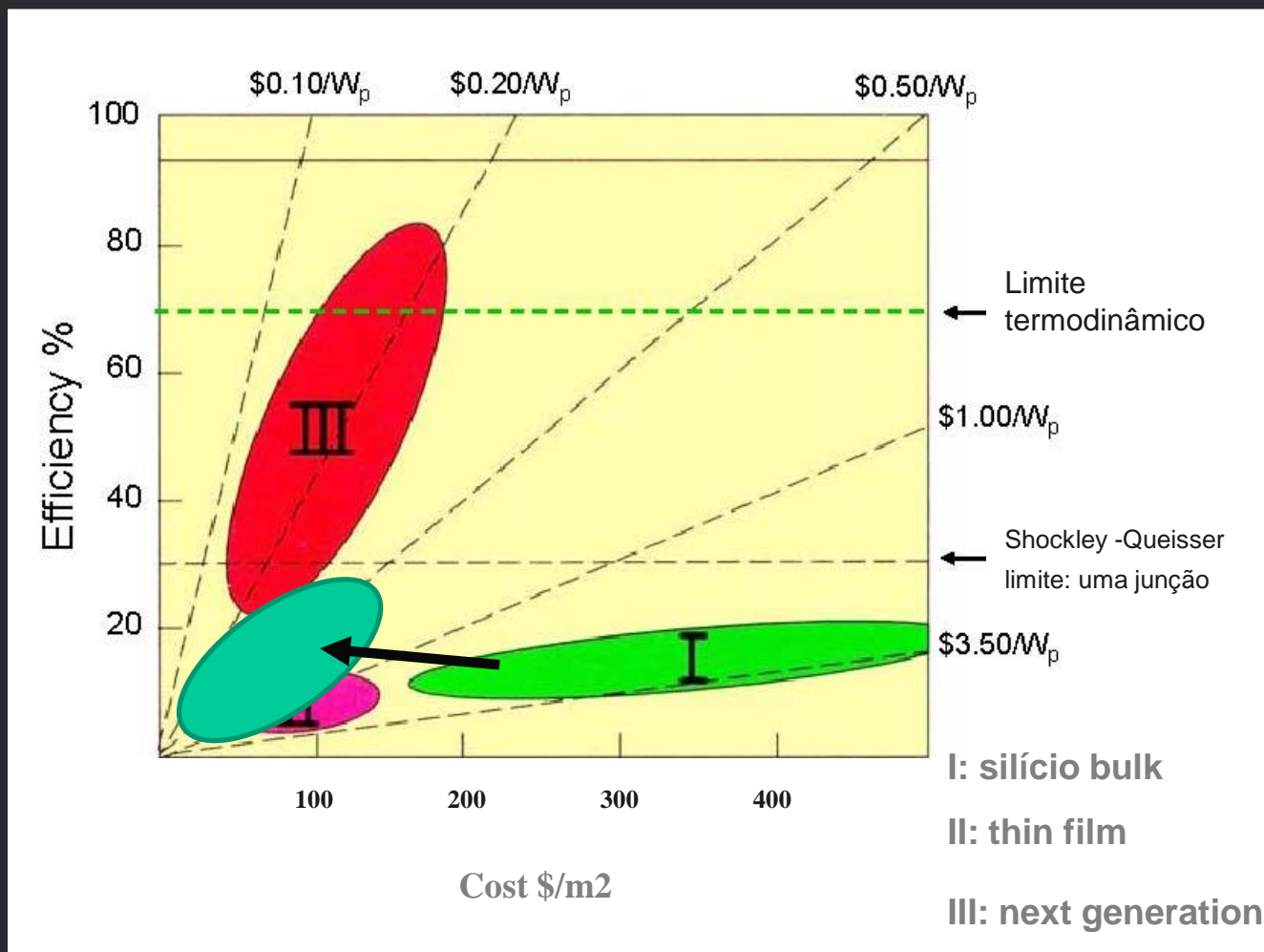


# PV Technology



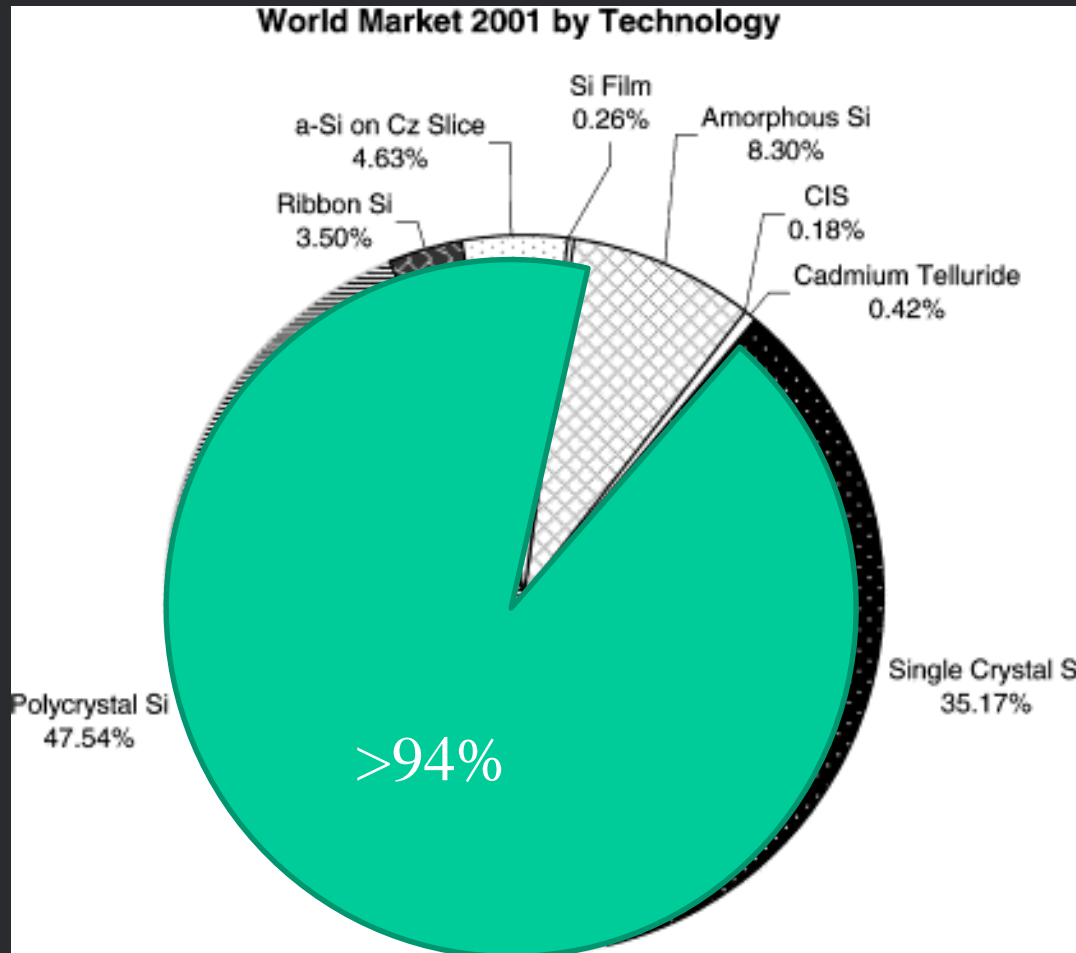
# PV Technology

## Visão da I&D no fotovoltaico



# PV Technology

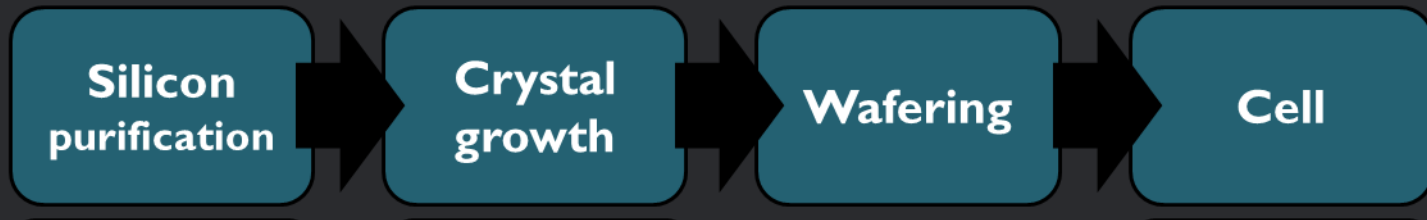
## Visão da I&D no fotovoltaico



# PV Technology

## Silicon PV flowchart

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# PV Technology

## First generation

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- Single layer of  $p-n$  diode
- One excitation per photon
- Mostly made of silicon wafers
- 95 % of solar cell market
- Life expectancy of >30 years
- Energy payback in 2-5 years (positive)

# PV Technology

## Second generation

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- “Thin-film” solar cells
- Mix of glass and ceramic with very little active semiconductor material
- Inexpensive but lower efficiency

# PV Technology

## Third generation

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- No  $p-n$  junction
- Organic polymer cells or nanocrystal cells
- Perovskites



# PV Technology

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- Production of Si

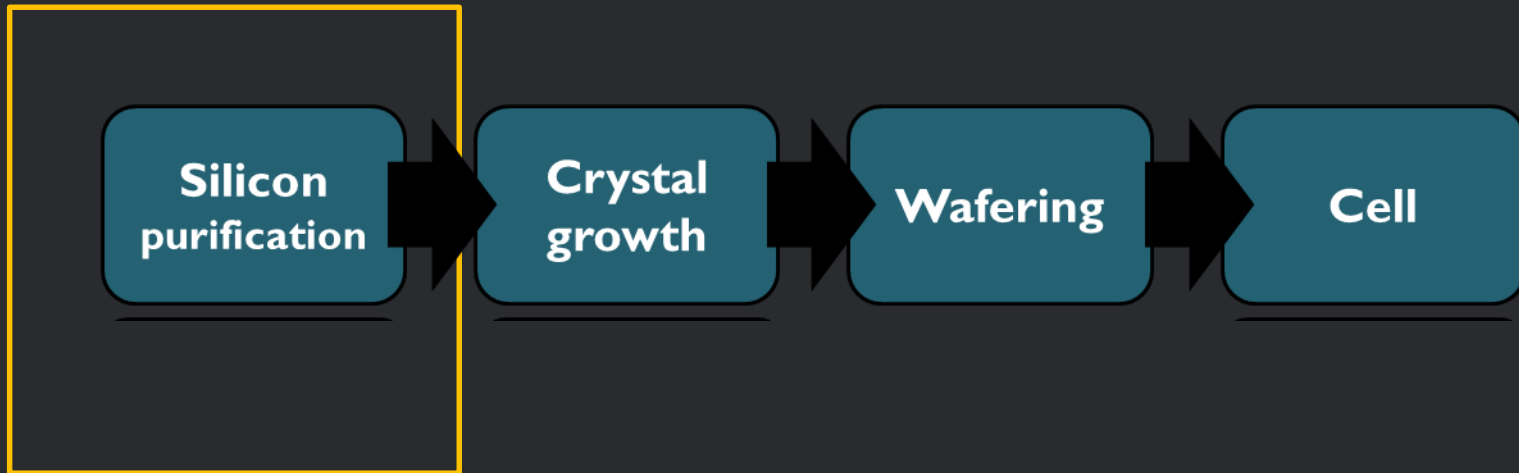
- Wafer based Si solar cells

- Thin-film solar cells

- Other concepts

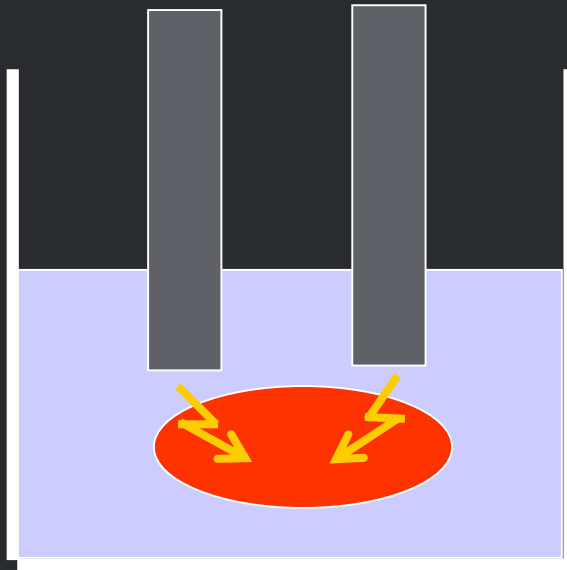
# PV Technology

## Silicon PV flowchart



# PV Technology

## MG silicon



Arc furnace

MG-Si is produced by reduction of  $\text{SiO}_2$  with C in arc furnace at 1800 C



# PV Technology

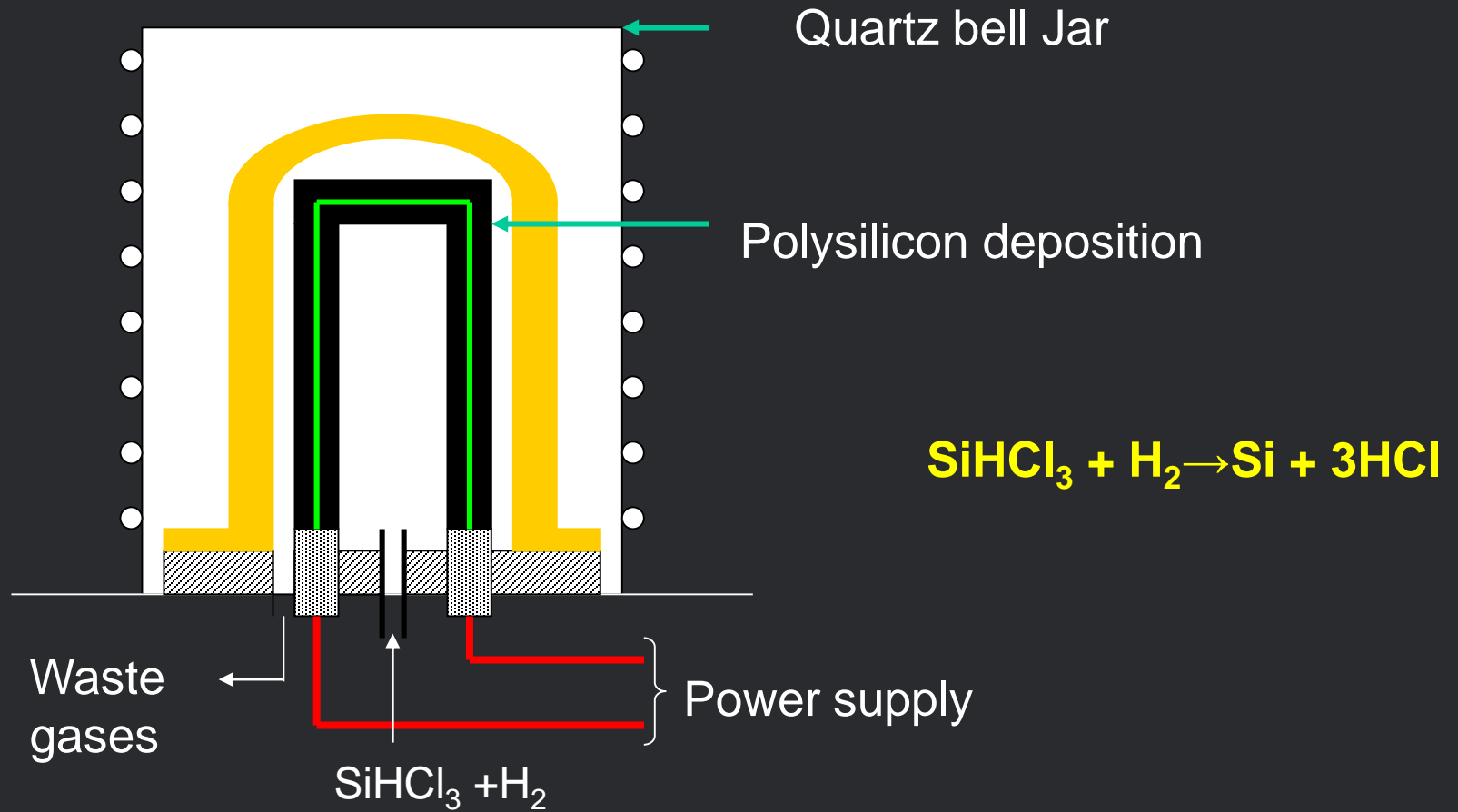
## MG silicon

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- MG-Si is material with 98-99% purity,
- Produced in about 1 Million tons per year
- Produced in countries which cheap electricity and quartz deposits (USA, Europe, Brazil, Australia, Norway)
  
- Average price is 2 to 4 \$/kg (electronic grade Si is 30-45 \$/kg )

# PV Technology

EG silicon



Siemens process

Deposition process is slow

# PV Technology

EG silicon

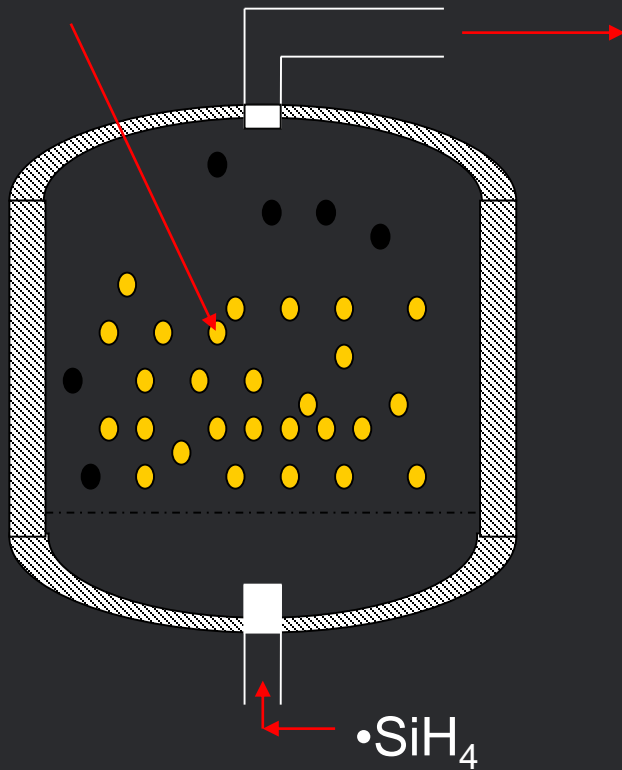
**Feedstock:** obtaining hyperpure silicon from gas phase



# PV Technology

## EG silicon

Granular Si



- Silicon seed particles are held in suspension by a gas mixture (H<sub>2</sub> and SiH<sub>4</sub>)
- At 600° C gas phase decomposition takes place, causing the seed particles to grow up to 2 mm in size
- Big particles falls due to weight
- Si is collected from the bottom of the jar

Continuous process

Fluidized bed reactor (FBR)

# PV Technology

## EG silicon

**Crystalline silicon** may be crystallized in PV in different forms:

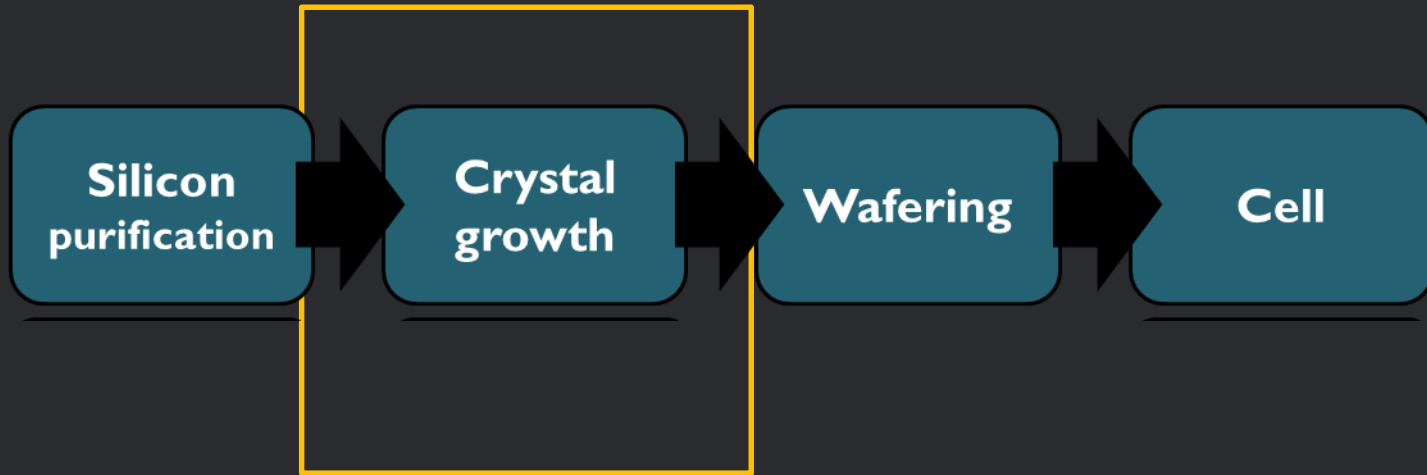
Descriptor	Symbol	Grain Size	Common Growth Techniques
Single crystal	sc-Si	>10cm	Czochralski (CZ) float zone (FZ)
Multicrystalline	mc-Si	1mm-10cm	Cast, sheet, ribbon
Polycrystalline	pc-Si	1 $\mu$ m-1mm	Chemical-vapour deposition
Microcrystalline	$\mu$ c-Si	<1 $\mu$ m	Plasma deposition



# PV Technology

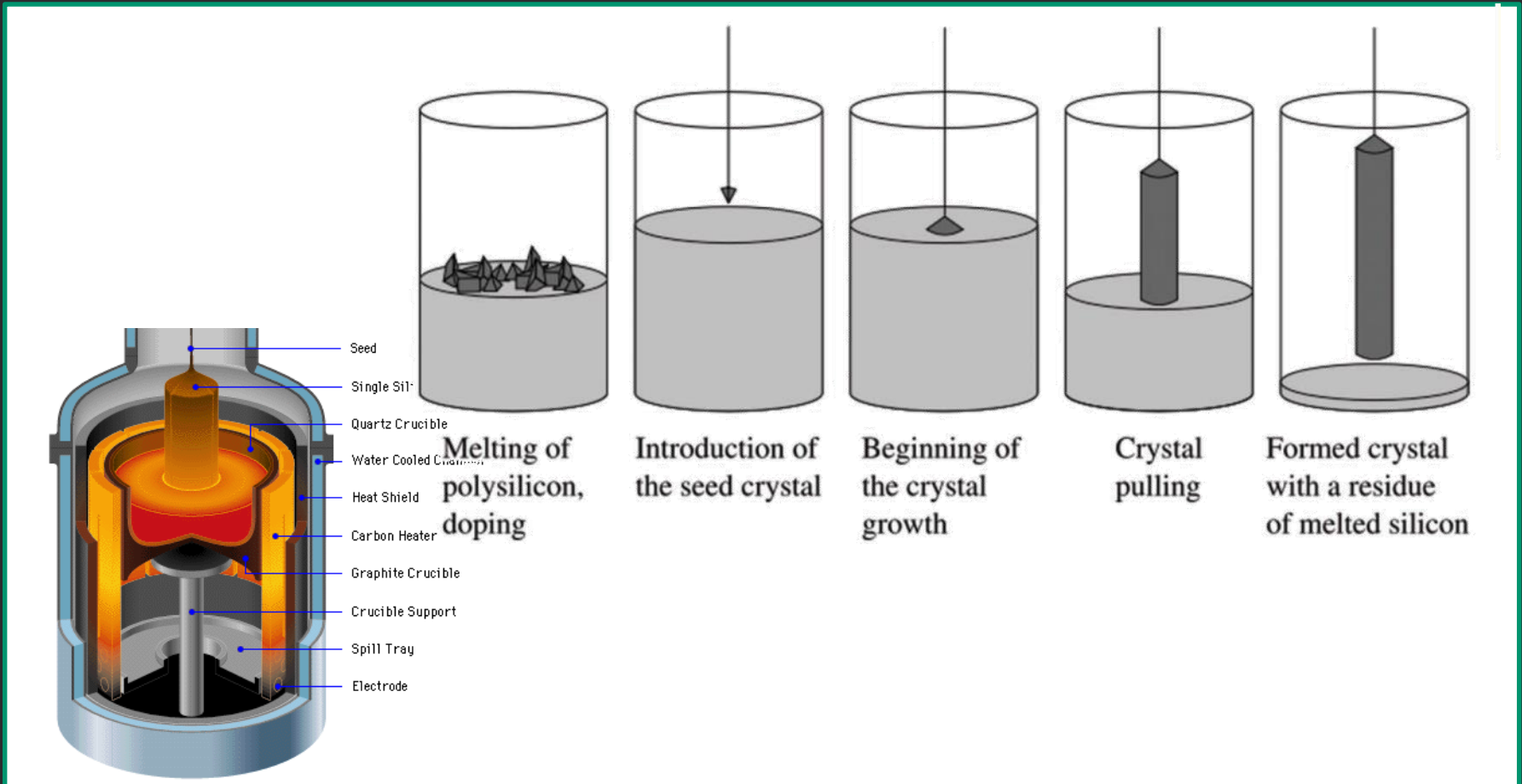
## Silicon PV flowchart

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# PV Technology

## Single crystal growth



Czochralski method

# PV Technology

## Single crystal growth



### Advantages

Good crystal quality

No induced thermal stresses

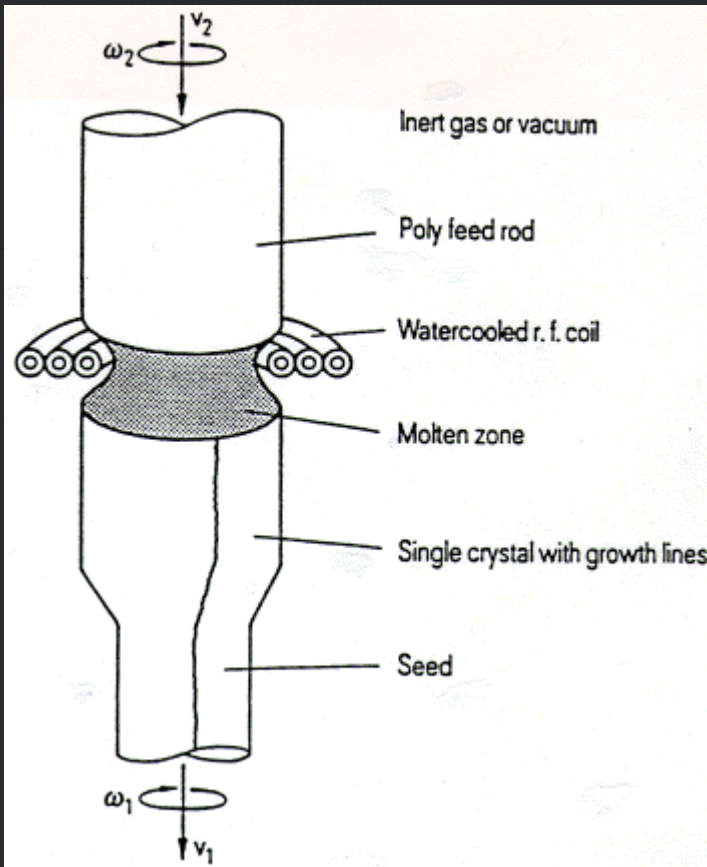
### Disadvantages

High oxygen content



# PV Technology

## Single crystal growth



Float zone method

### Advantages

Good crystal quality

No induced thermal stresses

No contamination

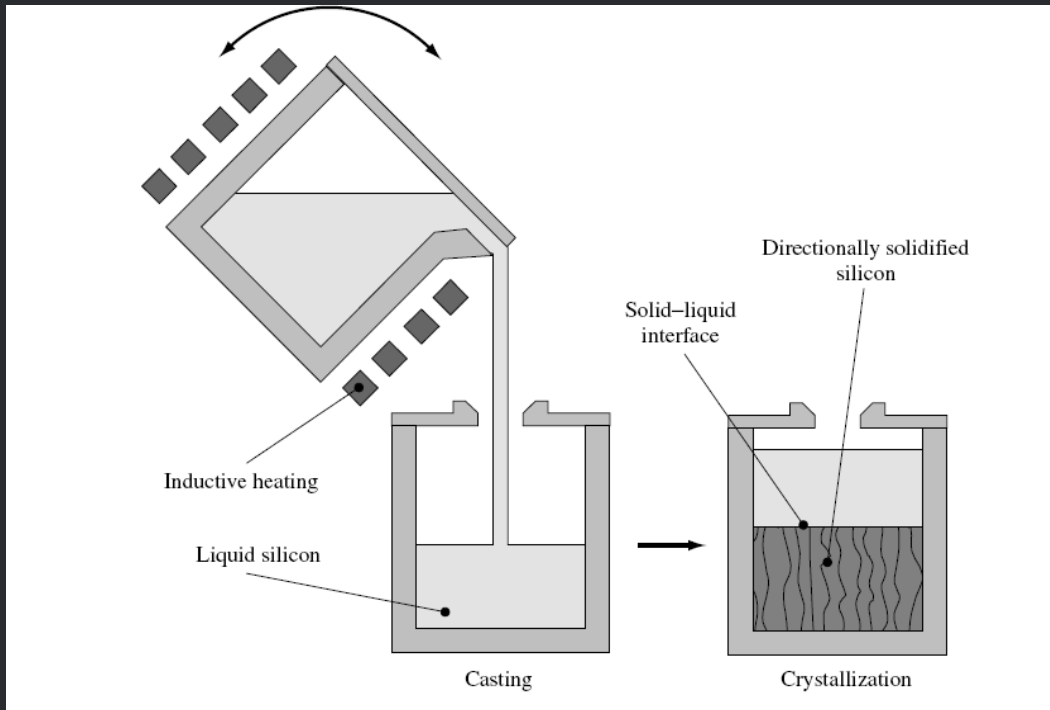
### Disadvantages

More expensive

Record efficiency solar cells  
have been manufactured  
with float zone

# PV Technology

## Multicrystalline silicon



### Advantages

Large volumes

Simple

### Disadvantages

Multicrystalline

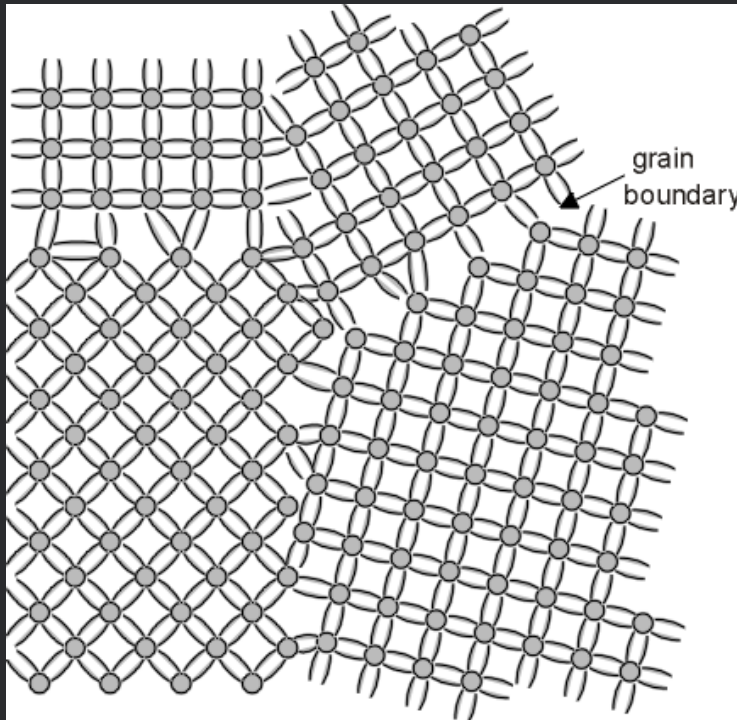
Impurity contamination

Crucible costs

# PV Technology

Mc silicon

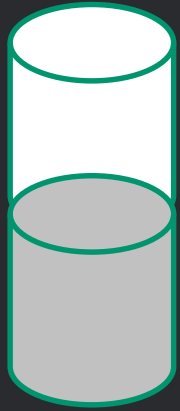
**Multicrystalline silicon** offers acceptable quality but at lower cost



Typical casting:  
240kg/56 hours

# PV Technology

## Bulk doping



liquid

solid

$$k_0 = \frac{C_s}{C_l},$$

**TABLE 1 EQUILIBRIUM SEGREGATION COEFFICIENTS FOR DOPANTS IN SI**

Dopant	$k_0$	Type	Dopant	$k_0$	Type
B	$8 \times 10^{-1}$	<i>p</i>	As	$3.0 \times 10^{-1}$	<i>n</i>
Al	$2 \times 10^{-3}$	<i>p</i>	Sb	$2.3 \times 10^{-2}$	<i>n</i>
Ga	$8 \times 10^{-3}$	<i>p</i>	Te	$2.0 \times 10^{-4}$	<i>n</i>
In	$4 \times 10^{-4}$	<i>p</i>	Li	$1.0 \times 10^{-2}$	<i>n</i>
O	1.25	<i>n</i>	Cu	$4.0 \times 10^{-4}$	— <sup>a</sup>
C	$7 \times 10^{-2}$	<i>n</i>	Au	$2.5 \times 10^{-5}$	— <sup>a</sup>
P	0.35	<i>n</i>			

<sup>a</sup>Deep-lying impurity level.

# PV Technology

## Bulk doping

$M_0$ - initial weight of liquid with dopant concentration  $C_0$   
 $S$  – dopant concentration in the melt

$$-dS = C_s dM.$$

$$C_l = \frac{S}{M_0 - M}.$$

$$k_0 = \frac{C_s}{C_l},$$

$$\frac{dS}{S} = -k_0 \left( \frac{dM}{M_0 - M} \right).$$



$M - M_0$

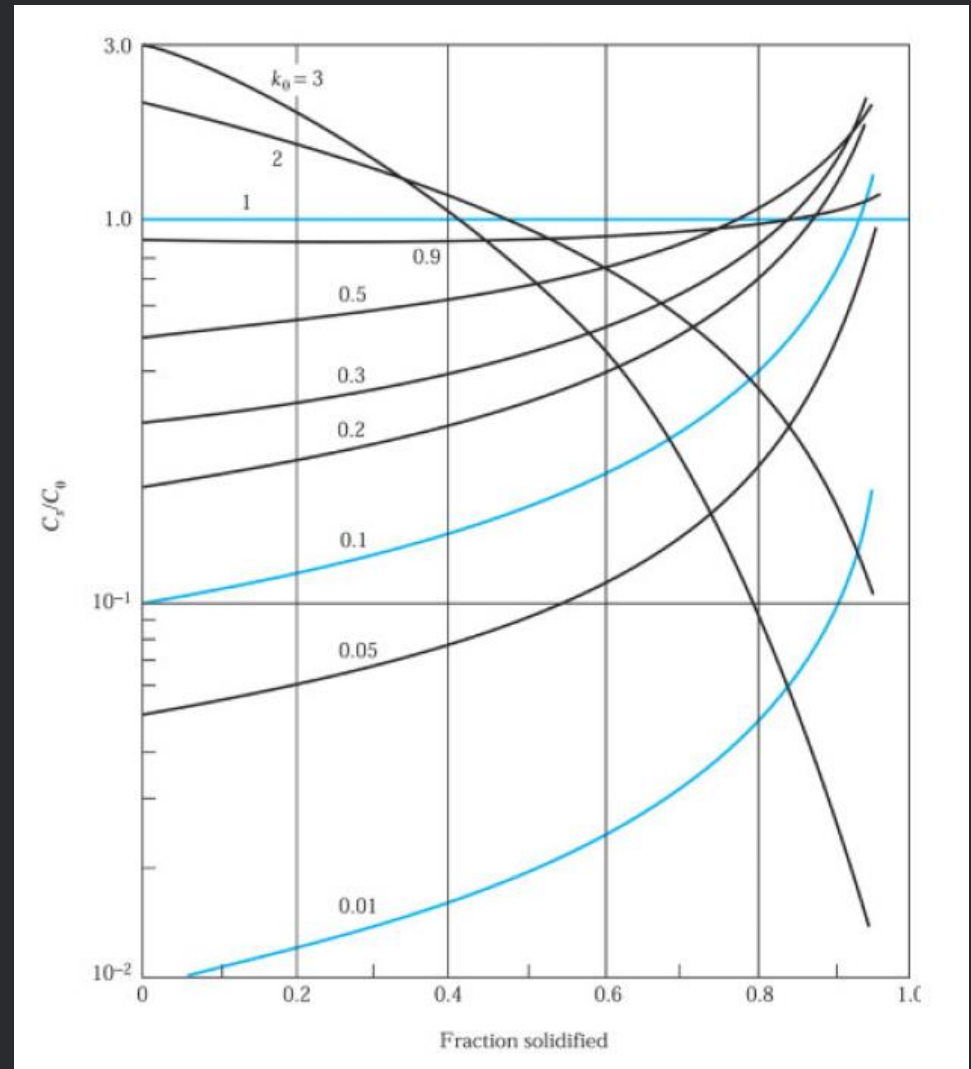
$dM$



# PV Technology

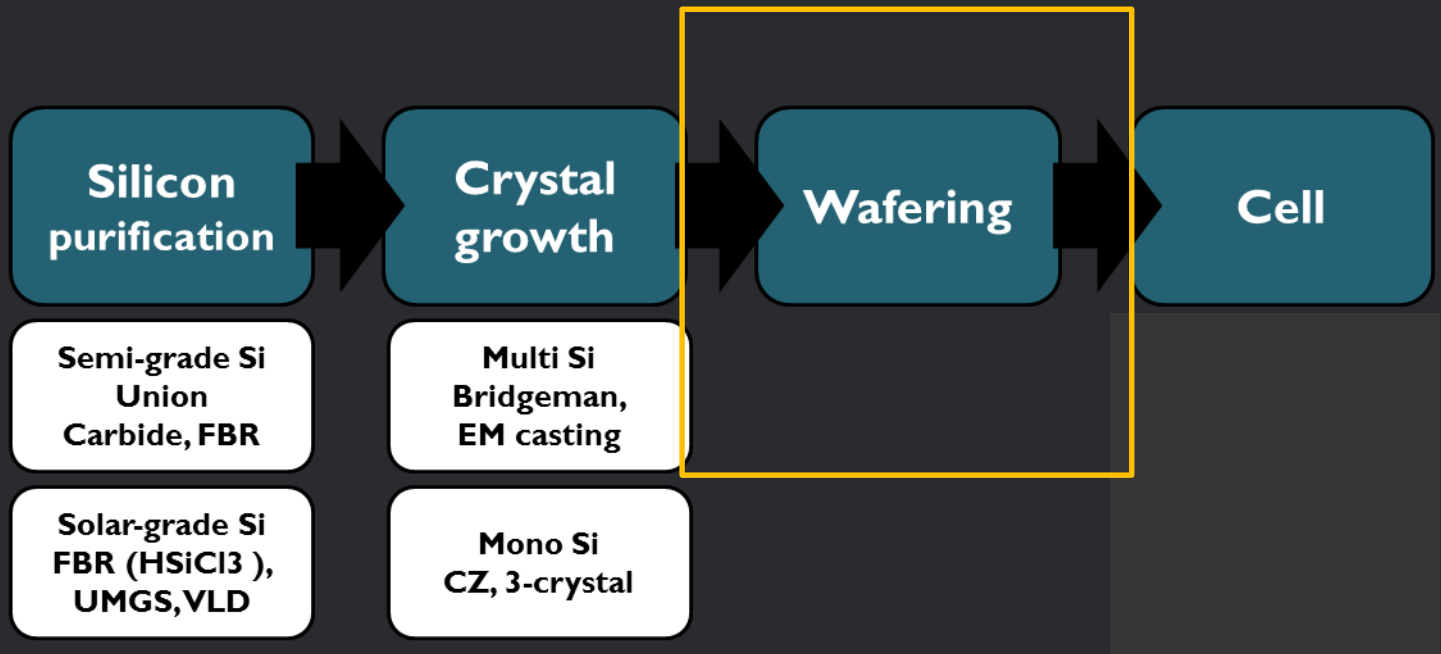
## Bulk doping

$$C_s = k_0 C_0 \left( 1 - \frac{M}{M_0} \right)^{k_0 - 1}$$



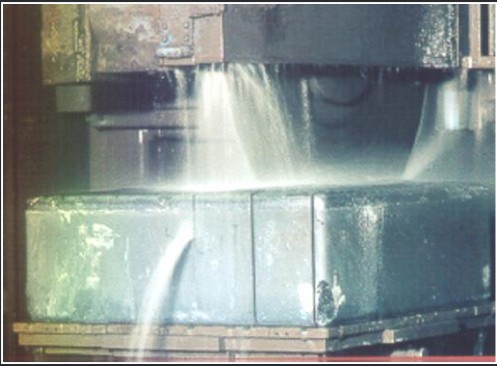
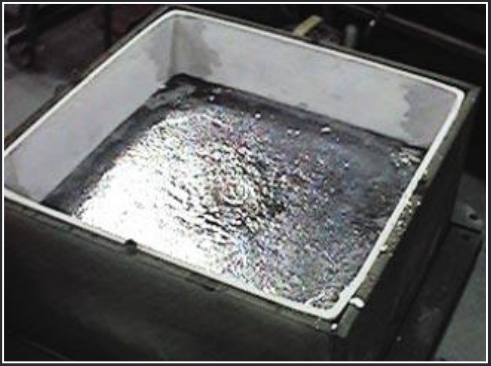
# PV Technology

## Silicon PV flowchart



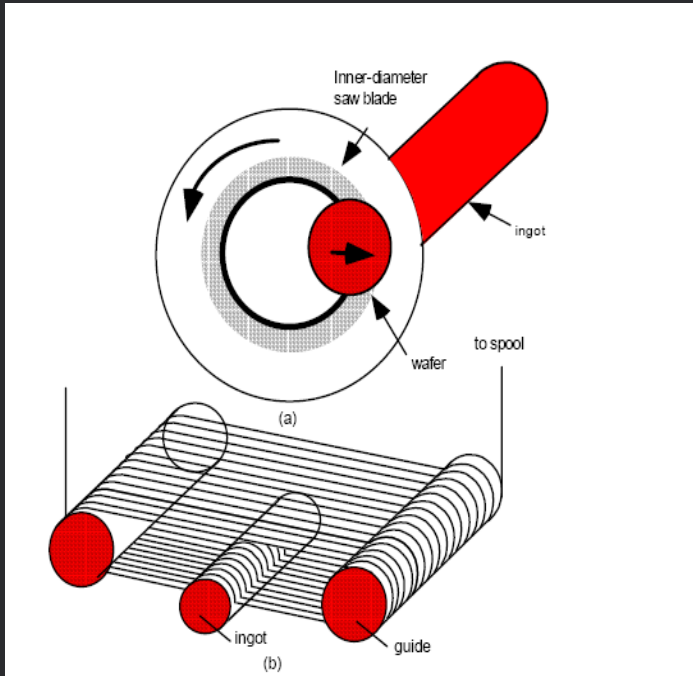
# PV Technology

Mc silicon

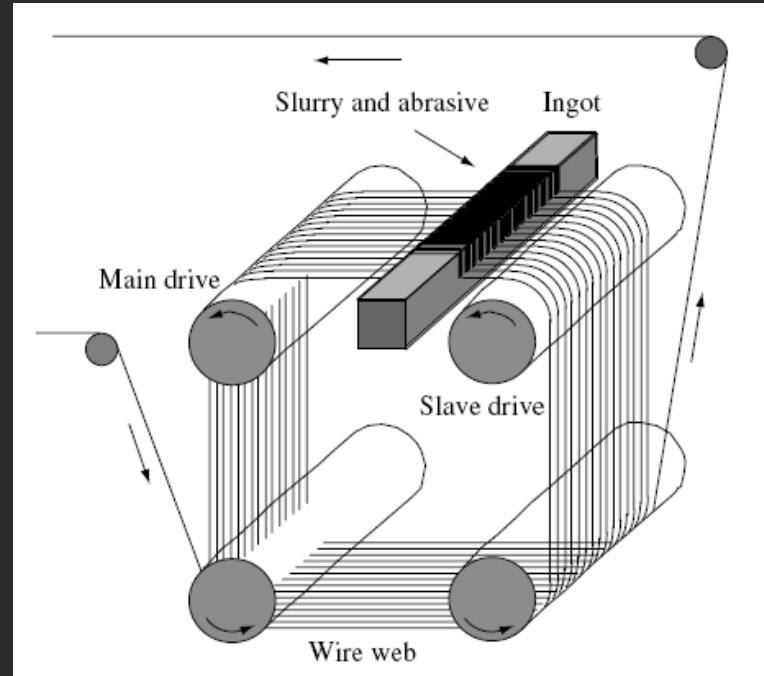


# PV Technology

## Wafering



Inner diameter sawing



Multi-wire sawing

### Disadvantages

Expensive

High kerf loss

# PV Technology

Ribbon technology

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Wafering is expensive!

Reducing material consumption by:

Producing thinner wafers

Reducing kerf loss

# PV Technology

## Ribbon technology

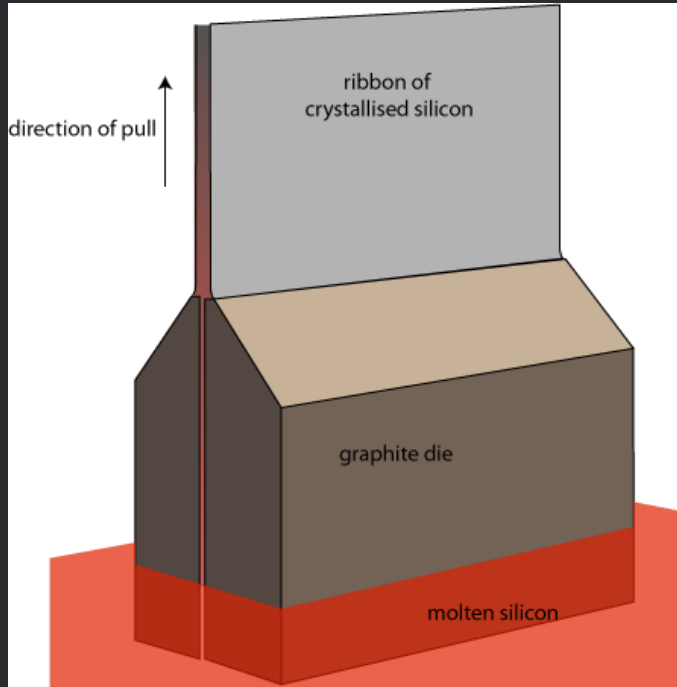
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Multicrystalline wafers may be grown directly in **sheet or ribbon** form.

- Edge defined film fed growth (EFG)
- String ribbon (SR)
- Ribbon growth on substrate (RGS)
- Dendritic web
- Sheet silicon
- ...

# PV Technology

## Ribbon technology

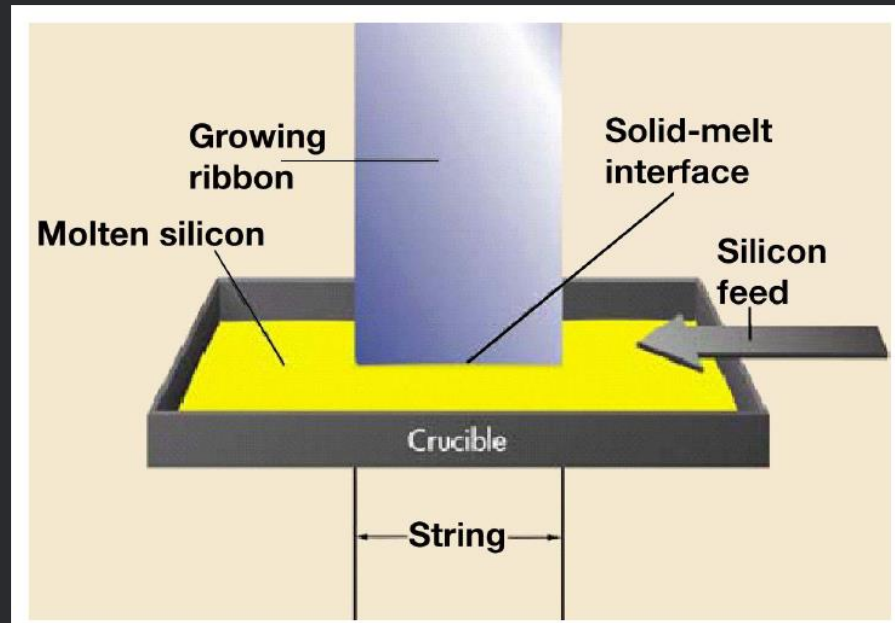


EFG



# PV Technology

## Ribbon technology

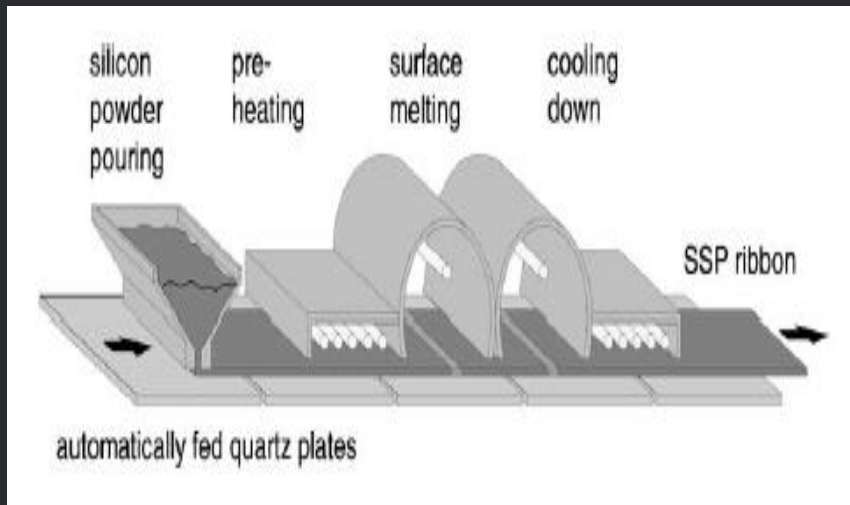


String ribbon

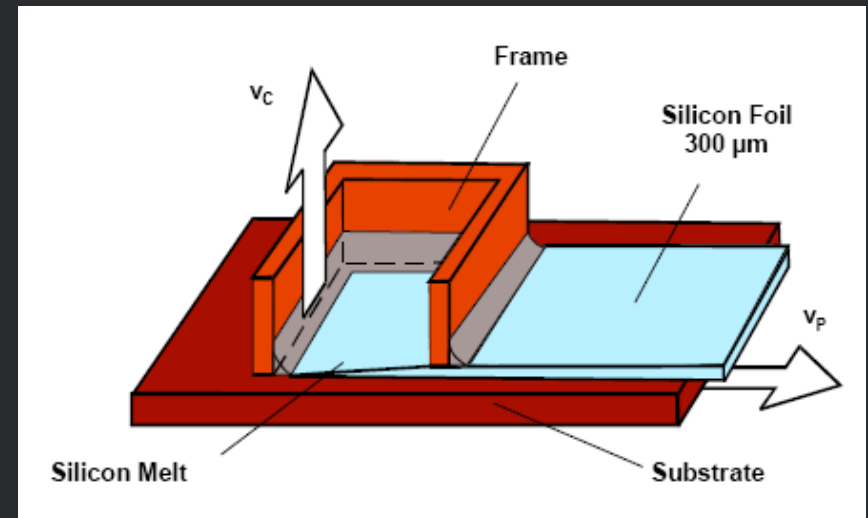


# PV Technology

## Ribbon technology



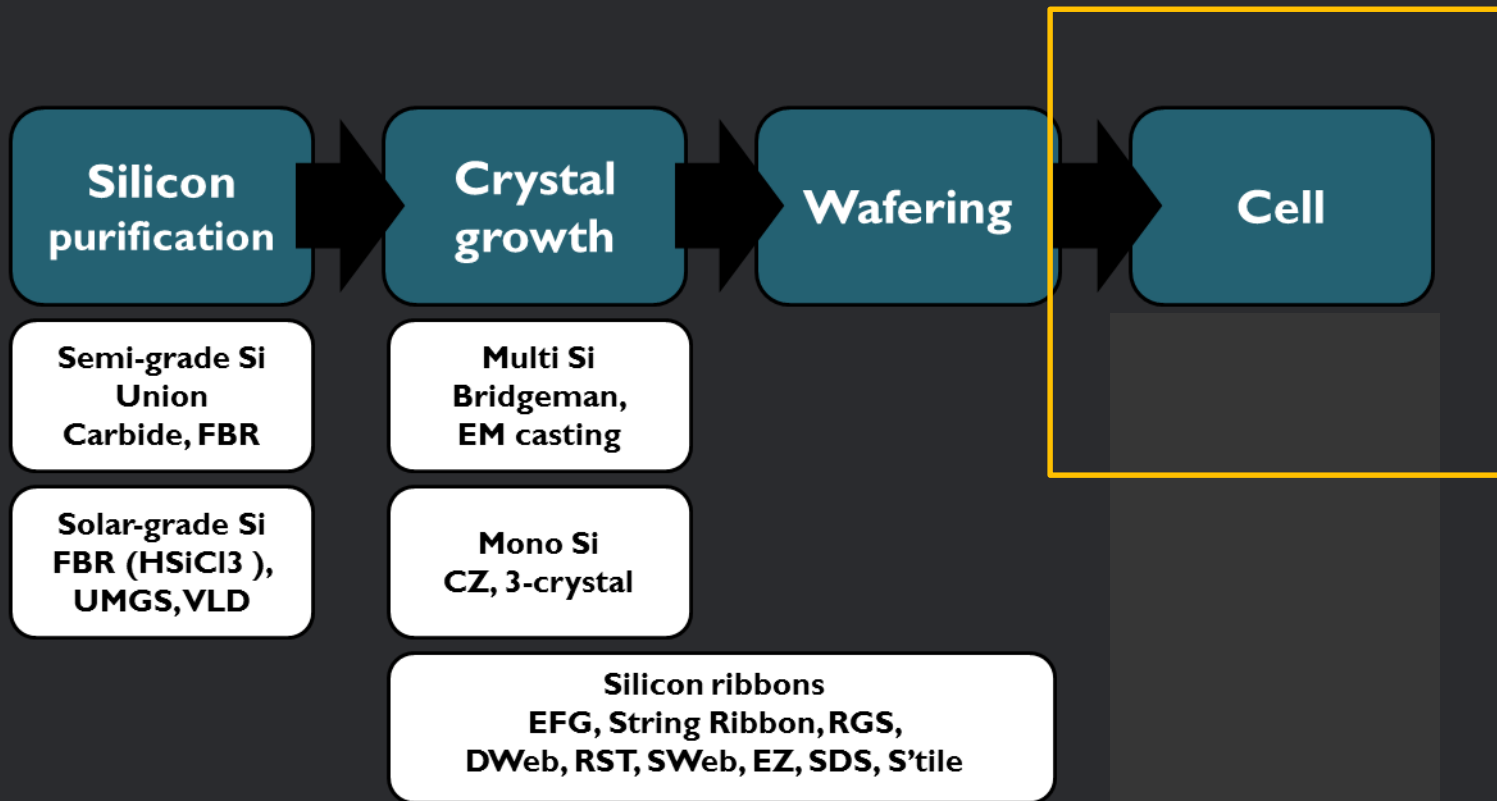
SSP



RGS

# PV Technology

## Technology overview



# PV Technology

Technology overview

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Next: Cell manufacturing